



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hongyong ZHANG

Serial No. 09/987,607

Filed: November 15, 2001

For: METHOD FOR PRODUCING

SEMICONDUCTOR INTEGRATED

CIRCUIT INCLUDING A THIN FILM

TRANSISTOR AND A CAPACITOR

) Group Art Unit: 2813

) Examiner: Jack S.J. Chen

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is  
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) Alexandria, VA 22313-1450, on January 31,  
) 2008.

) 

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

This Information Disclosure Statement is being submitted with an RCE. Therefore, no fee is required.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,



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